AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listing of claims in the application.

Listing of Claims

1. (Original) An organic EL element drive circuit for current-driving organic EL elements through terminal pins provided correspondingly to R, G and B display colors of an organic EL display panel in a display time period and resetting terminal voltages of said organic EL elements in a reset time period, according to a timing control signal for regulating the display time period corresponding to one horizontal scan period and the reset time period corresponding to a retrace period of the horizontal scan, said organic EL element drive circuit comprising a pulse generator circuit for generating the timing control signal having the reset time period, which is set according to data set externally of said pulse generator circuit, correspondingly to respective R, G and B display colors, a display luminance of each display color on a screen of said organic EL display panel being regulated according to the data.

2. (Original) An organic EL element drive circuit as claimed in claim 1, wherein the reset time period of the timing control signal for R display color is longer than the reset time period of the timing control signal for each of G and B display colors.

3. (Original) An organic EL element drive circuit as claimed in claim 2, further comprising a plurality of reference current generator circuits for generating reference currents corresponding to respective R, G and B display colors, output currents supplied to said terminal pins being produced on the basis of the reference currents, wherein display luminance corresponding to each of R, G and B display colors on said screen is regulated by regulating the reference currents.

4. (Original) An organic EL element drive circuit as claimed in claim 3, wherein said pulse generator circuits are provided correspondingly to respective R, G and B display colors and the data is inputted externally of said organic EL element drive circuit.

5. (Original) An organic EL element drive circuit as claimed in claim 4, wherein each said pulse generator circuit includes a counter for counting clocks the number of which corresponds to the set data and the timing control signal is generated according to the count value of said counter.

6. (Original) An organic EL element drive circuit as claimed in claim 5, further comprising first D/A converter circuits provided correspondingly to said terminal pins and current sources for generating output currents for driving said organic EL elements, said first D/A converter circuits for respective R, G and B display colors receiving the reference currents corresponding to respective R, G and B display colors or drive currents generated on the basis of the reference currents and display data, D/A converting the display data according to the reference currents or the drive currents and driving said current sources according to the current obtained by the D/A conversion, switch circuits provided correspondingly to output terminals of said current sources for generating the output currents, each said organic EL element being reset when said switch circuit corresponding thereto is turned ON according to said timing control signal.

7. (Original) An organic EL element drive circuit as claimed in claim 6, wherein said current sources are constructed with a current mirror circuit, said reference current generator circuits include three reference current generator circuits provided correspondingly to respective R, G and B display colors, a value of the reference current generated by each said reference current generator circuit being set according to data set externally of said reference current generator circuit.

8. (Original) An organic EL element drive circuit as claimed in claim 7, wherein each said reference current generator circuit includes a second D/A converter circuit for D/A converting the data set externally of said reference current generator circuit, the data being inputted externally of said organic EL element drive circuit.

9. (Original) An organic EL element drive circuit as claimed in claim 6, wherein the timing control signals for respective G and B display colors are identical.

10. (Original) An organic EL element drive circuit as claimed in claim 9, wherein said current sources are constructed with a current mirror circuit, said reference current generator circuits include three reference current generator circuits provided correspondingly to respective R, G and B display colors, a value of the reference current generated by each said reference current generator circuit being set according to data set externally of said reference current generator circuit.

11. (Original) An organic EL element drive circuit as claimed in claim 10, wherein each said reference current generator circuit includes a second D/A converter circuit for D/A converting the data set externally of said reference current generator circuit, the data being inputted externally of said organic EL element drive circuit.

12. (Original) An organic EL element drive circuit as claimed in claim 11, wherein said reference current generator circuits for respective G and B display colors are constructed with a single reference current generator circuit and the timing control signal for G and the timing control signal for B are a same pulse.

13. (Previously Presented) An organic EL display device comprising an organic EL element drive circuit as claimed in claim 1.

14. (Original) An organic EL display device as claimed in claim 13, wherein said organic EL element drive circuit is provided as an IC chip and said control circuit is provided as an IC chip externally of said organic EL element drive IC.

15. (Original) An organic EL display device as claimed in claim 14, further comprising a processor and the data set in said pulse generator circuits and the data set in said second D/A converter circuits are supplied by said processor according to data inputted to said processor.

16. (Original) An organic EL display device as claimed in claim 15, wherein said pulse generator circuit is a programmable pulse generator circuit for generating a pulse having programmable width.